	Application No.	Amplicant/o)	
Nation of Allowability	Application No. Applicant(s)		
	10/680,815	KIM ET AL.	
Notice of Allowability	Examiner	Art Unit	
	Joseph D. Torres	2133	
The MAILING DATE of this communication appearance All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED or other appropriate comn IGHTS. This application is	in this application. If not included nunication will be mailed in due cours	e. THIS ne initiative
1. \boxtimes This communication is responsive to <u>the Response to Elec</u>	tion / Restriction Filed 08/	21/2006.	
2. The allowed claim(s) is/are 1-8 and 14-16.			
 3. Acknowledgment is made of a claim for foreign priority un a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 		or (f).	
2. Certified copies of the priority documents have		ion No. 10/074.422 .	
3. ☐ Copies of the certified copies of the priority doc	• •		om the
International Bureau (PCT Rule 17.2(a)).		ou in the national stage application in	5111 410
* Certified copies not received:			
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	of this communication to fi IENT of this application.	e a reply complying with the requiren	nents
4. A SUBSTITUTE OATH OR DECLARATION must be subminification (PTO-152) which give	itted. Note the attached Exes reason(s) why the oath	(AMINER'S AMENDMENT or NOTIC or declaration is deficient.	E OF
5. CORRECTED DRAWINGS (as "replacement sheets") mus	st be submitted		
(a) ☐ including changes required by the Notice of Draftspers		ew (PTO-948) attached	
1) hereto or 2) to Paper No./Mail Date	-	,	
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date	s Amendment / Comment o	or in the Office action of	
Identifying indicia such as the application number (see 37 CFR 1. each sheet. Replacement sheet(s) should be labeled as such in the	.84(c)) should be written on he header according to 37 C	the drawings in the front (not the back)	of
6. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT I	sit of BIOLOGICAL MAT FOR THE DEPOSIT OF B	ERIAL must be submitted. Note the IOLOGICAL MATERIAL.	те
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Attachment(s) 1. ☐ Notice of References Cited (PTO-892)	5. ☐ Notice of I	nformal Patent Application	
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	6. X Interview	Summary (PTO-413),	
3. M Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date 6/24/05,3/15/04		./Mail Date <u>20060929</u> . s Amendment/Comment	
Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. 🛭 Examiner	s Statement of Reasons for Allowance	е
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//PRIMAR	Y EXAMINER 3Y CENTER 2100	Joseph D. Torres, PhD Primary Examiner Art Unit: 2133	
U.S. Patent and Trademark Office PTOL-37 (Rev. 08-06)	tice of Allowability	Part of Paper No./Mail Da	te 20060929
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EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Michael Musella on 9/28/2006.

The application has been amended as follows:

As per claim 1:

In line 11 of claim 1, the phrase "alternately arranging the symbols" was replaced with --alternately arranging the <u>interleaved</u> symbols--.

In line 13 of claim 1, the term –interleaved-- was inserted before the word "parity".

As per claim 14:

In line 7 of claim 14, the phrase "multiplexing the symbols of" was replaced with - multiplexing the <u>interleaved</u> symbols of--.

EXAMINER'S STATEMENT OF REASONS FOR ALLOWANCE

The following is an examiner's statement of reasons for allowance: The present invention pertains to an apparatus using a Turbo encoder for channel encoding data followed by a channel interleaver used for mitigating the effects of burst errors followed

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by a rate matcher for matching interleaved symbols to a particular communication channel. Claim 1 recites various features: "a turbo encoder having a plurality of constituent encoders, for receiving information bits, generating a plurality of parity symbol sequences according to a given code rate by encoding the information bits, and outputting an information symbol sequence and the plurality of parity symbol sequences, each of the constituent encoders for generating at least one parity symbol sequence, the at least one parity symbol sequence from one constituent encoder corresponding to the at least one parity symbol sequence from another constituent encoder; a channel interleaver for individually interleaving the information symbol sequence and the parity symbol sequences, alternately arranging the interleaved symbols of the corresponding parity symbol sequences, and serially concatenating the interleaved information symbol sequence and the arranged interleaved parity symbol sequences; and a QCTC generator for generating a subcode of a QCTC by repeating the serially concatenated symbol sequence and selecting a predetermined number of symbols from the repeated symbol sequence according to code rate and selection information." [Emphasis Added] Analogously, claim 14 recites "a turbo encoder for receiving information bits, generating a plurality of parity symbol sequences by encoding the information bits, and outputting an information symbol sequence and the plurality of parity symbol sequences; a channel interleaver for individually interleaving the information symbol sequence and the parity symbol sequences, generating new parity symbol sequences by multiplexing the interleaved symbols of parity symbol sequences, and serially

concatenating the information symbol sequence and the new parity symbol sequences; and a QCTC generator for generating a sub-code of a QCTC with a given code rate by selecting a predetermined number of symbols from the serially concatenated symbol sequence at a given starting position according to the code rate." [Emphasis Added]

The Prior Art of record (for example, Tong; Wen et al. US 6744744 B1, hereafter referred to as Tong) teaches a Turbo encoder 90 in Figure 5 of Tong for channel encoding data followed by a channel interleaver 93 used for mitigating the effects of burst errors followed by a rate matcher 94 for matching interleaved symbols to a particular communication channel.

The prior art however are not concerned with and do not teach, suggest, or otherwise render obvious a means and circuit arrangement for rearranging and selecting the channel interleaved symbols as taught by claims 1 and 14. Hence the prior art taken alone or in any combination fail to teach the claimed novel feature in claims 1 and 14.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Joseph D. Torres whose telephone number is (571) 272-3829. The examiner can normally be reached on M-F 8-5.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Albert Decady can be reached on (571) 272-3819. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Joseph D. Torres, PhD Primary Examiner Art Unit 2133

JOSEPH D. TORRES PRIMARY EXAMINER